

Historie Firmware^{v001}

Datum:	27.02.2012	Version (Historie):	1.6
Betreff:	FW V3.55 for Speed7 CPU's	Erstellt:	Karsten Schmidt

Effective immediately, the firmware v3.55 for the Speed7 CPUs are available and can be download under www.vipa.de/de/service-support/downloads/firmware.
The firmware is compatible with their predecessors.

Further information and downloads can be found at www.vipa.de

Changes for CPUs

1. No delayed analog signal processing at C-CPU
2. SFC14/SFC15 on CAN-Master now OK
3. FORCE order is retained even after changing the IP address
4. Size limit of PROTECT.WLD by CPU memory
5. Multi-instance on SFB14/SFB15 possible
6. When compressing cycle time constant of SFC25